

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: H. Ohtani Art Unit: 2816

Serial No.: 09/389,393 Examiner: B. William Baumeister

Filed : September 3, 1999

Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Commissioner for Patents Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant calls attention to the enclosed document listed on the attached form PTO-1449.

Kindly accept this Information Disclosure Statement under Rule 97(c)(1).

The rule 17(p) certification fee of \$240 is enclosed.

Applicant has advised that they are having prepared a full translation of the enclosed Japanese document and it will be forwarded in due course. A brief abstract is provided herein at this time.

Consideration of the foregoing and enclosure plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's

## CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 16, 2000

Date of Deposit

Signature

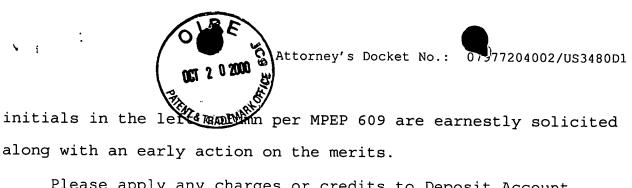
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Respectfully submitted,

%cott C. Harris Reg. No. 32,030

SCH/smr

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